

**GOVERNMENT COLLEGE OF ENGINEERING, Jalgaon.**  
**Department of Electronics and Telecommunication Engineering**  
**Proposed Scheme for M. Tech. Electronics and Telecommunication Engineering (Digital Systems)**  
**Semester - I**

Course Code	Course Name	Teaching Scheme Hrs/week			Evaluation Scheme							Credits
					Theory				Practical			
		TH	TUT	PR	ISA	ISE1	ISE2	ESE	ICA	ESE	Total	
ET501	Image Processing & Computer Vision	3	--	--	10	15	15	60	--	--	100	3
ET502	Advanced Digital Signal Processing	3	--	--	10	15	15	60	--	--	100	3
ET503	Embedded Systems	3	--	--	10	15	15	60	--	--	100	3
ET504	Advanced Computer Architectures	3	--	--	10	15	15	60	--	--	100	3
ET 505	<b>Elective I</b>	3	--	--	10	15	15	60	--	--	100	3
ET 506	Image Processing & Computer Vision and Advanced Digital Signal Processing LAB	--		2	--	--	--	--	25	25	50	1
ET 507	Embedded Systems and Advanced Computer Architectures LAB	--	--	2	--	--	--	--	25	25	50	1
ET 508	Seminar-I	--	--	2	--	--	--	--	50	--	50	2
ET 509	Self Study	--	--	--	--	--	--	--	--	--	100	4
	<b>Total</b>	<b>15</b>	<b>--</b>	<b>6</b>	<b>50</b>	<b>75</b>	<b>75</b>	<b>300</b>	<b>100</b>	<b>50</b>	<b>750</b>	<b>23</b>

**TH:** Theory Lecture

**ISA:** Internal Sessional Assessment

**ESE:** End Semester Examination

**TUT:** Tutorial

**ISE:** In Semester Examination

**PR:** Practical

**ICA:** Internal Continuous Assessment

**Elective I**

- A. Statistical Information Processing
- B. Biomedical Engineering
- C. Artificial Intelligence and Neural Networks

# GOVERNMENT COLLEGE OF ENGINEERING, Jalgaon.

Department of Electronics and Telecommunication Engineering

Proposed Scheme for M. Tech. Electronics and Telecommunication Engineering (Digital Systems)

Semester - II

Course Code	Course Name	Teaching Scheme			Evaluation Scheme							Credits
		Hrs/week			Theory				Practical			
		TH	TUT	PR	ISA	ISE1	ISE2	ESE	ICA	ESE	Total	
ET551	Fuzzy Logic Systems	3	--	--	10	15	15	60	--	--	100	3
ET552	Digital Communication Systems	3	--	--	10	15	15	60	--	--	100	3
ET553	Modern Digital System Design & HDL	3	--	--	10	15	15	60	--	--	100	3
ET554	Computer Networks	3	--	--	10	15	15	60	--	--	100	3
ET 555	<b>Elective II</b>	3	--	--	10	15	15	60	--	--	100	3
ET 556	Digital Communication Systems and Modern Digital System Design & HDL LAB	--	--	2	--	--	--	--	25	25	50	1
ET 557	Fuzzy Logic System and Computer Networks LAB	--	--	2	--	--	--	--	25	25	50	1
ET 558	Seminar-II	--	--	2	--	--	--	--	50	--	50	2
ET 559	Self Study	--	--	--	--	--	--	--	--	--	100	4
	<b>Total</b>	<b>15</b>	<b>--</b>	<b>6</b>	<b>50</b>	<b>75</b>	<b>75</b>	<b>300</b>	<b>100</b>	<b>50</b>	<b>750</b>	<b>23</b>

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**Elective II**

- A. Pattern Recognition and Classification
- B. Biomedical Signal Processing
- C. Nanotechnology

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**Semester - III**

Course Code	Course Name	Teaching Scheme Hrs/week			Evaluation Scheme							Credits
					Theory				Practical			
		TH	TUT	PR	ISA	ISE1	ISE2	ESE	ICA	ESE	Total	
ET601	Dissertation-I	--	--	--*	--	--	--	--	100	50	150	8
ET602	Intellectual Property Right	1	1	--	--	--	--	--	50	--	50	2
<b>Total</b>		1	1	--	--	--	--	--	150	50	200	10

**Semester - IV**

Course Code	Course Name	Teaching Scheme Hrs/week			Evaluation Scheme						Total	Credits
					Theory				Practical			
		TH	TUT	PR	ISA	ISE1	ISE2	ESE	ICA	ESE		
ET651	Dissertation –II	--	--	---**	--	--	--	--	100	200	300	14

**TH:** Theory Lecture

**TUT:** Tutorial

**PR:** Practical

**ISA:** Internal Sessional Assessment

**ISE:** In Semester Examination

**ICA:** Internal Continuous Assessment

**ESE:** End Semester Examination

\*10 Hours laboratory work plus 4 hour guide contact hr and max 3 students per guide per year

\*\*12 Hours laboratory work plus 4 hour guide contact hr and max 3 students per guide per year